

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| Applicant(s): | Kannan V. Ramanath et al.  | )                              |
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| Serial No.:   | 08/939,844   | ) Art Unit: 1753<br>)          |
| Filed:        | September 29, 1997   | ) Examiner: K. Mayejar         |
| Title:        | Cadmium-Free Junction Fabrication  Process for Culpse. Thin Film Solar Cells | )<br>)<br>) Atty Dkt No. 07.10 |

## **CERTIFICATE OF MAILING UNDER 37 CFR § 1.8**

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